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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Pat nt Application

Inventor(s) : Robert Warren Schmidt  
Serial No. : 09/661,506  
Filed : September 13, 2000  
Atty. Dkt. : RCA 89,970  
Title : VOLTAGE LEVEL TRANSLATION CIRCUITS  
Examiner : Anh Quan Tra  
Art Unit : 2816

**AMENDMENT**

Mail Stop Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

In response to the Office Action dated September 11, 2003, please  
amend the above-identified application and enter the remarks as follows.

**Listing and Amendments to the Claims** begin on page 2 of this paper.

**Remarks/Arguments** begin on page 5 of this paper.

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